

Electronic Acknowledgement Receipt

EFS ID:	2919324
Application Number:	10776101
International Application Number:	
Confirmation Number:	2835
Title of Invention:	HIGH SPEED POWER-GATING TECHNIQUE FOR INTEGRATED CIRCUIT DEVICES INCORPORATING A SLEEP MODE OF OPERATION
First Named Inventor/Applicant Name:	Kim C. Hardee
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Filer Authorized By:	William J. Kubida
Attorney Docket Number:	UMI-360
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 1770
RAM confirmation Number	1624
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	DOC109.PDF	36897	no	1
			df1f85bf8c8458cc371d240762e22f9d849b870ea		

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	8519	no	2
			205668c0d7dd961fa5ba6086016851899b15eecc		

Warnings:**Information:**

Total Files Size (in bytes):			45416
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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